

# FPGA

Had a discussion with a trusted mentor of mine - spanning hardware and software home based work today - specifically about restarting FPGA work I looked at in 2010.

Move to OPENCL for FPGA based

<https://www.xilinx.com/content/dam/xilinx/imgs/products/zyng/zyng-eg-block.PNG>

\$249

<https://www.xilinx.com/products/boards-and-kits/1-vad4rl.html>

voucher for 1 year of

60d-eval/\$1k

<https://www.xilinx.com/products/design-tools/software-zone/sdsoc.html>

<https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/18842491/Boards+and+Kits+Voucher+Licensing>

or

\$700 PCI based

<https://www.xilinx.com/products/boards-and-kits/ek-s6-sp605-g.html>

or

\$40 Lattice based 7k lookup table

<https://www.sparkfun.com/products/14829>

Deprecate all non-FPGA work below

<http://javahardware.blogspot.com/>

<http://kilocore.blogspot.com/>

<http://obrienlabs.blogspot.com/2010/12/altera-cyclone-iii-development-kits.html>

<https://eclipsejpa.blogspot.com/2012/11/java-7-se-parallel-processing-cluster.html>